

#6/A
9-28-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
SANCHES ET AL.

Serial No. **Not Yet Assigned**

Filing Date: **Herewith**

For: **DIGITAL SIGNAL PROCESSOR
WITH PARALLEL ARCHITECTURE**

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DATE OF DEPOSIT: July 26, 2001

NAME: Alex Greene

SIGNATURE: Alex Greene

PRELIMINARY AMENDMENT

Director, U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Prior to the calculation of fees and examination of
the present application, please enter the amendments and
remarks set out below.

In the Drawings:

Submitted herewith is a request for a proposed
drawing modification as indicated in red ink to label FIG. 1
as prior art.

In the Claims:

Please cancel Claims 1 to 19.

Please add new Claims 20 to 59.

20. A signal processor for executing variable-sized
instructions, each instruction comprising up to N codes, the
signal processor comprising: